

Dual Conductor, High Current Power InductorsFlat-Pac[™] FPT705 Series









Description

- Halogen free, lead free, RoHS compliant
- 125°C maximum total operating temperature
- 8.0 x 7.1 x 5.35mm maximum surface mount package
- Ferrite core material
- Dual conductor, two-turn construction
- Inductance range from 150nH to 300nH

Applications

 Designed specifically for use with Picor® Cool-Power® ZVS-Buck Regulator Family (Picor part number Series Pl33xx and Pl34xx)

Environmental Data

- Storage temperature range: -40°C to +125°C
- Operating temperature range: -40°C to +125°C (ambient + self-temperature rise)
- Solder reflow temperature: J-STD-020D compliant

Packaging

 Supplied in tape-and reel packaging, 1400 parts per 13" diameter reel

		Product S	pecifications		
Part Number ⁶	OCL1 ±10% (nH)	FLL ² min. (nH)	I _{rms} ³ (Amps)	I _{sat} ⁴ @ 25°C (Amps)	DCR5 (mΩ) @ 20°C
FPT705-150-R	150	135		35	
FPT705-170-R	170	153		31	
FPT705-190-R	190	171		28	
FPT705-200-R	200	180	13	25	0.65 ± 0.15
FPT705-230-R	230	207		23	
FPT705-270-R	270	243		19	
FPT705-300-R	300	270		17	

- 1. Open Circuit Inductance (OCL) test parameters: 100kHz, $0.1V_{rms}$, $0.0Adc @ 25^{\circ}C$ (Pins 1-3, short 2-4).
- 2. Full Load Inductance (FLL) test parameters: 100kHz, 0.1V $_{\rm rms}$, l $_{\rm sat}1$ @ 25°C (Pins 1-3, short 2-4).
- 3. I_{rms}: DC current for an approximate temperature rise of 40°C without core loss. Derating is necessary for AC currents. PCB layout, trace thickness and width, air-flow, and proximity of other heat-generating components will affect the temperature rise. It is recommended that the temperature of the part not exceed 125°C under worst case operating conditions verified in the end application.
- 4. I_{sat}1: Peak current for approximately 2% rolloff at +25℃.
- 5. DCR Tested from Pins (1-2) and (3-4) @ 20°C.
- 6. Part Number Definition: FPT705-xxx-R FPT705 = Product code and size xxx= Inductance value in nH "-R" Suffix = RoHS compliant



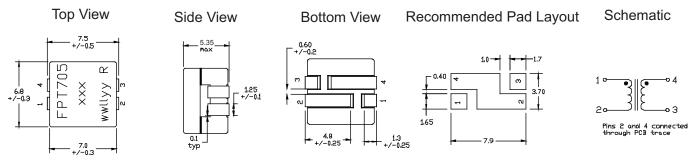
Inselkammerstraße 10 D-82008 Unterhaching Tel: +49 (0)89 614 503 10 Fax +49 (0)89 614 503 20 E-Mail: power@hy-line.de URL: www.hy-line.de Gründenstrasse 82 CH-8247 Flurlingen Tel: +41 (0)52 647 42 00 Fax +41 (0)52 647 42 01 E-Mail: power@hy-line.ch URL: www.hy-line.ch



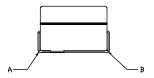
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Dimensions - mm



All soldering surfaces to be coplanar within 0.10 millimeters.

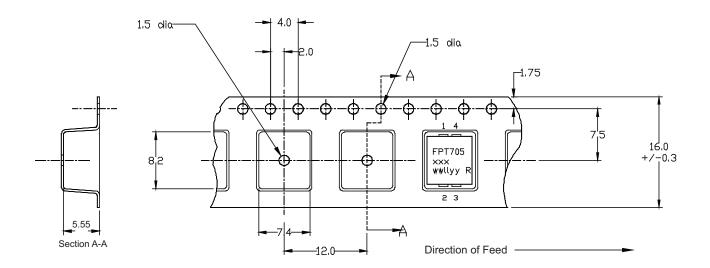


DCR is measured between points "A" and point "B"

Part Marking:

- FPT705 (Product code and size)
- xxx = (Inductance=(Inductance value in nH)
- wwllyy= Date Code, R= Revision level

Packaging Information - mm



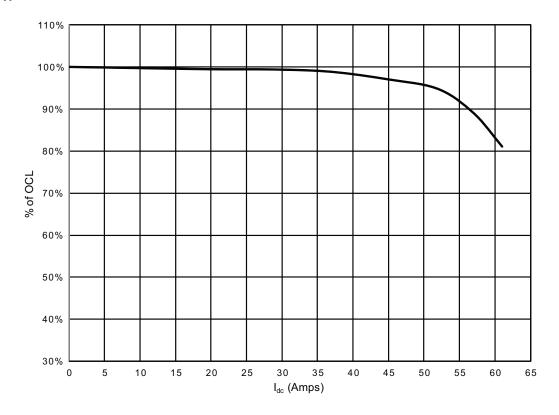
Supplied in tape and reel packaging, 1400 parts on a 13" diameter reel.

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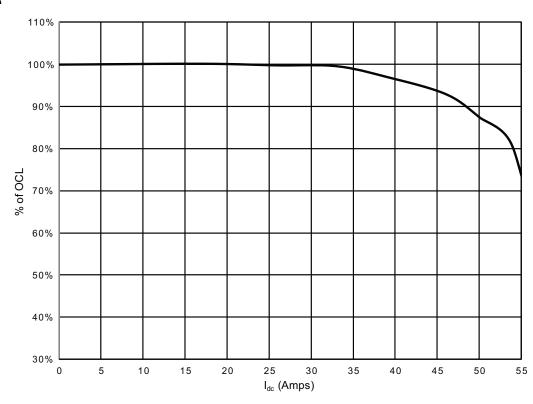


Inductance Characteristics

FPT705-150-R



FPT705-170-R

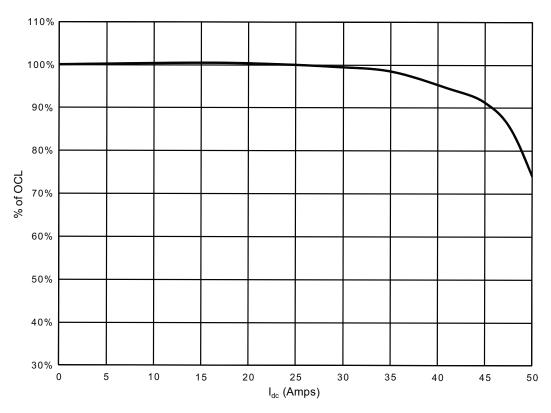


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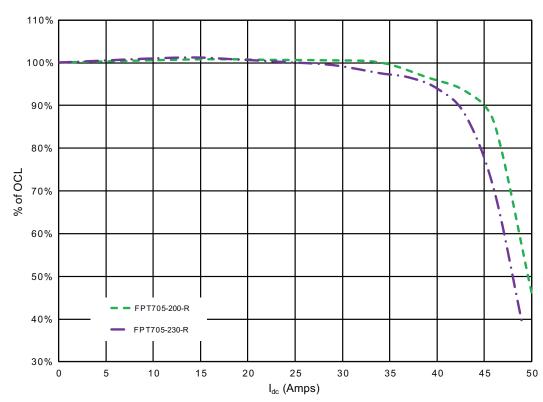


Inductance Characteristics

FPT705-190-R



FPT705-200-R & -230-R

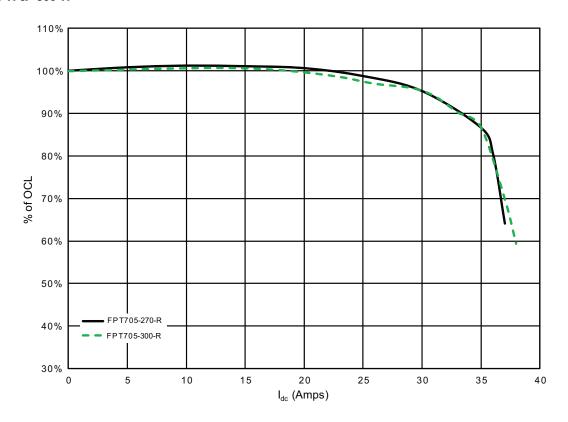


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Inductance Characteristics

FPT705-270-R & -300-R



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Solder Reflow Profile

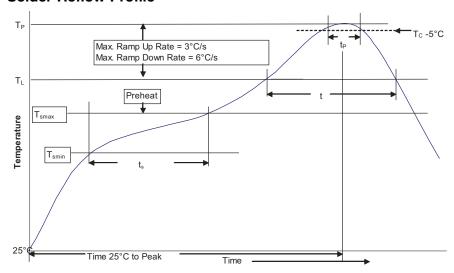


Table 1 - Standard SnPb Solder (T_c)

		Volume	Volume	
	Package	mm ³	mm ³	
	Thickness	<350	≥350	
	<2.5mm	235°C	220°C	
	≥2.5mm	220°C	220°C	

Table 2 - Lead (Pb) Free Solder (Tc)

Package Thickness	Volume mm³ <350	Volume mm ³ 350 - 2000	Volume mm³ >2000
<1.6mm	260°C	260°C	260°C
1.6 - 2.5mm	260°C	250°C	245°C
>2.5mm	250°C	245°C	245°C

Reference JDEC J-STD-020D

Profile Feature		Standard SnPb Solder	Lead (Pb) Free Solder	
Preheat and Soak	• Temperature min. (T _{smin})	100°C	150°C	
	 Temperature max. (T_{smax}) 	150°C	200°C	
	• Time (T _{smin} to T _{smax}) (t _s)	60-120 Seconds	60-120 Seconds	
Average ramp up rate T _{Smax} to T _p		3°C/ Second Max.	3°C/ Second Max.	
Liquidous temperature (TL) Time at liquidous (t _L)		183°C 60-150 Seconds	217°C 60-150 Seconds	
Peak package body temperature (Tp)*		Table 1	Table 2	
Time (t _p)** within 5 °C of the specified classification temperature (T _C)		20 Seconds**	30 Seconds**	
Average ramp-down rate (T _p to T _{smax})		6°C/ Second Max.	6°C/ Second Max.	
Time 25°C to Peak Temperature		6 Minutes Max.	8 Minutes Max.	

 $^{^{\}star}$ Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

^{**} Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.



Inselkammerstraße 10 D-82008 Unterhaching Tel: +49 (0)89 614 503 10 Fax +49 (0)89 614 503 20 E-Mail: power@hy-line.de URL: www.hy-line.de Gründenstrasse 82 CH-8247 Flurlingen Tel: +41 (0)52 647 42 00 Fax +41 (0)52 647 42 01 E-Mail: power@hy-line.ch URL: www.hy-line.ch

North America

Cooper Electronic Technologies 1225 Broken Sound Parkway NW Boca Raton, FL 33487-3533 Tel: 1-561-998-4100 Fax: 1-561-241-6640 Toll Free: 1-888-414-2645 Cooper Bussmann P.O. Box 14460 St. Louis, MO 63178-4460 Tel: 1-636-394-2877 Fax: 1-636-527-1607

Europe

Cooper Electronic Technologies Cooper (UK) Limited Burton-on-the-Wolds Leicestershire • LE12 5TH UK Tel: +44 (0) 1509 882 737 Fax: +44 (0) 1509 882 786 Cooper Electronic Technologies Avda. Santa Eulalia, 290 08223

Terrassa, (Barcelona), Spain Tel: +34 937 362 812 +34 937 362 813

+34 937 362 813 Fax: +34 937 362 719

Asia Pacific

Cooper Electronic Technologies 1 Jalan Kilang Timor #06-01 Pacific Tech Centre Singapore 159303 Tel: +65 278 6151 Fax: +65 270 4160

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