

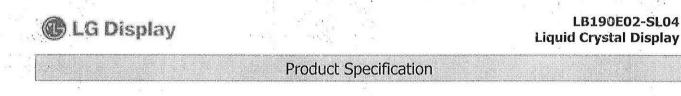
TFT-DISPLAY DATENBLATT

LG Display Modell: LB190E02-SL04

KURZDATEN:

Hersteller	LG Display
Diagonale	19"
Format	5:4
Auflösung	1280 x 1024
Backlight	LED / 700 cd/m²
Interface	LVDS
Touchscreen	nein
Temperatur	0+50 °C (Betrieb)

HY-LINE Computer Components Vertriebs GmbH Inselkammerstr. 10, 82008 Unterhaching bei München Tel.: +49 89 614 503 40 | Fax: +49 89 614 503 50 computer@hy-line.de | www.hy-line.de/computer



SPECIFICATION FOR **APPROVAL**

- **Preliminary Specification**)

Final Specification

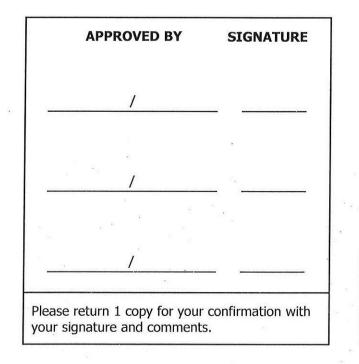
Title

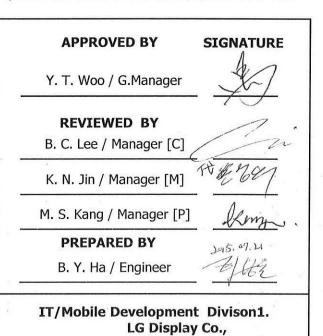
Customer	
MODEL	

19" SXGA TFT LCD

SUPPLIER	LG Display Co., Ltd.
*MODEL	LB190E02
Suffix	SL04

*When you obtain standard approval, please use the above model name without suffix





Jul. 21. 2015

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RECORD OF REVISIONS

Revision No	Revision Date	Page	Description		
0.0	Mar. 12. 2015	-	First Draft, Preliminary Specifications		
0.1	Apr. 21. 2015	4, 23	Change Weight information		
		24,25	Change LCM drawing		
		28	Change Packing Form, a) Packing quantity in one box		
1.0	Jul. 21. 2015	24	Change Bezel open area		
		-	Final Draft		

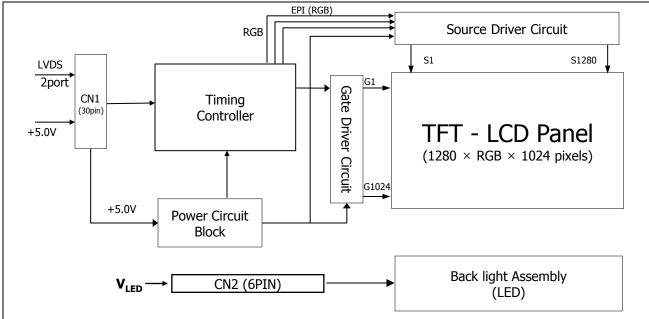


1. General Description

LB190E02 is a Color Active Matrix Liquid Crystal Display with a Light Emitting Diode (White LED) backlight system without LED driver. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 19.0 inch diagonally measured active display area with SXGA resolution (1024 vertical by 1280 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16,7M colors

It has been designed to apply the 8Bit 2 port LVDS interface.

It is intended to support displays where high brightness, super wide viewing angle, high color saturation, and high color are important.



General Features

[Figure 1] Block diagram

Active Screen Size	19.0 inch (481.84mm) diagonal
Active Screen Size	
Outline Dimension	400.0(H) x 330.0(V) x 13.8(D) mm(Typ.)
Pixel Pitch	0.294 mm x 0.294mm
Pixel Format	1280 horiz. by 1024 vert. Pixels. RGB stripe arrangement
Color Depth	16,7M colors
Luminance, White	700 cd/m ² (Center 1 Point, Typ.)
Viewing Angle(CR>10)	View Angle Free (R/L 178(Typ.), U/D 178(Typ.))
Power Consumption	Total 24 Watt (Typ.) (4 Watt @VLCD, 20 Watt @W/O Driver)
Weight	1550 g (typ.) / 1600 g(Max.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(3H), Advanced Anti-Glare treatment of the front polarizer

2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Parameter	Symbol	Valu	ies	Units	Notes	
Falameter	Symbol	Min	Max	Units		
Power Input Voltage	VLCD	-0.3	6.0	Vdc	at 25 \pm 2°C	
Operating Temperature	Тор	0	50	°C		
Storage Temperature	Тѕт	-20	60	°C	1 2 2	
Operating Ambient Humidity	Нор	10	90	%RH	1, 2, 3	
Storage Humidity	Нѕт	10	90	%RH		
LCM Surface Temperature (Operation)	T _{Surface}	0	65	°C	1, 4	

Note : 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39 °C Max, and no condensation of water.

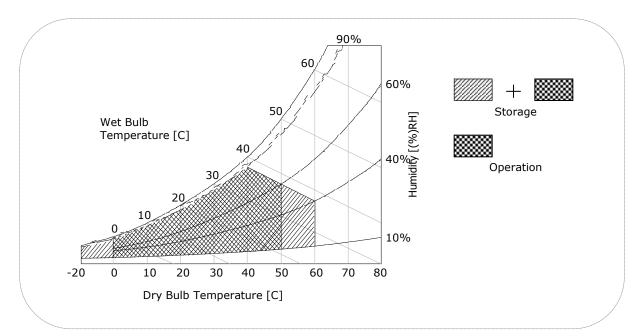
2. Maximum Storage Humidity is up to 40°C, 70% RH only for 4 corner light leakage Mura.

3. Storage condition is guaranteed under packing condition

4. LCM Surface Temperature should be Min. 0°C and Max. 65°C under the VLCD=5.0V,

fV=60Hz, 25°C ambient Temp. no humidity control and LED string current is typical value.

FIG.2 Temperature and relative humidity



3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the LED/Backlight, is typically generated by a LED Driver. The LED Driver is an external unit to the LCDs.

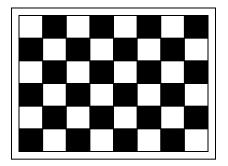
Table 2-1. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Values	Unit	Notos		
Parameter	Symbol	Min	Тур	Max	Unit	Notes	
MODULE :							
Power Supply Input Voltage	VLCD	4.5	5.0	5.5	Vdc		
Dowor Cupply Input Current	ILCD	-	800	1040	mA	1	
Power Supply Input Current		-	1100	1430	mA	2	
Dower Concumption	Рс ТҮР	-	4.0	5.2	Watt	1	
Power Consumption	Рс мах	-	5.5	7.15	Watt	2	
Differential Impedance	Zm	90	100	110	Ohm		
Rush current	Irush	-	-	3.0	А	3	

Note :

- 1. The specified current and power consumption are under the V_{LCD} =5.0V, 25 ± 2°C, f_v=60Hz condition whereas Typical Power Pattern[Mosaic] shown in the [Figure 3] is displayed.
- 2. The current is specified at the maximum current pattern.
- 3. Maximum Condition of Inrush current :

The duration of rush current is about 5ms and rising time of power Input is 500us \pm 20%.(min.).





Typical power Pattern

Maximum power Pattern

FIG.3 Mosaic pattern & White Pattern for power consumption measurement

Table 2-2. LED Bar ELECTRICAL CHARACTERISTICS

Davamatar	Symbol	Condition		llait	Notes		
Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit	Notes
LED :							
LED String Current	Is		-	130	-	mA	1,2,7
LED String Voltage	Vs		-	38.4	40.8	V	1,3,7
Power Consumption	PBar		-	20.0	21.2	Watt	4,5,7
LED Life Time	LED_LT		50,000	-	-	Hrs	6,7

LED driver design guide

1) The design of the LED driver must have specifications for the LED in LCD Assembly.

The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED driver.

So all the parameters of an LED driver should be carefully designed and output current should be Constant current control.

Please control feedback current of each string individually to compensate the current variation among the strings of LEDs.

When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the LED driver (no lighting, flicker, etc) never occurs.

When you confirm it, the LCD module should be operated in the same condition as installed in your instrument.

- 2) LGD recommend that Dimming Control Signal (PWM Signal) should be synchronized with Frame Frequency for Wavy Noise Free.
- 1. Specified values are for a single LED bar.
- 2. The specified current is defined as the input current for a single LED string with 100% duty cycle.
- 3. The specified voltage is input LED string voltage at typical 130 mA 100% duty current.
- 4. The specified power consumption is input LED bar power consumption at typical 130 mA 100% duty current.
- 5. The LED bar power consumption shown above does not include loss of external driver.

The used LED bar current is the LED typical current. Max Power Consumption is calculated with $P_{Bar} = Vs(Max.) \times Is(Typ) \times No.$ of strings.

- 6. This lifetime is the estimated value, and is not guarantee value. The lifetime is determined as the time when brightness of LED package is 50% or less than the initial value at the typical LED current on condition of continuous operating at $25 \pm 2^{\circ}$ C.
- 7. LED operating DC Forward Current must not exceed LED Max Ratings at 25 \pm 2°C

3-2. Interface Connections

3-2-1. LCD Module

- LCD Connector(CN1). : GT103-30S-H23 (LSM)

- Mating Connector : FI-X30C2L (Manufactured by JAE) or Equivalent

No	Symbol	Description	No	Symbol	Symbol
1	FR0M	Minus signal of odd channel 0 (LVDS)	16	SR1P	Plus signal of even channel 1 (LVDS)
2	FR0P	Plus signal of odd channel 0 (LVDS)	17	GND	Ground
3	FR1M	Minus signal of odd channel 1 (LVDS)	18	SR2M	Minus signal of even channel 2 (LVDS)
4	FR1P	Plus signal of odd channel 1 (LVDS)	19	SR2P	Plus signal of even channel 2 (LVDS)
5	FR2M	Minus signal of odd channel 2 (LVDS)	20	SCLKINM	Minus signal of even clock channel (LVDS)
6	FR2P	Plus signal of odd channel 2 (LVDS)	21	SCLKINP	Plus signal of even clock channel (LVDS)
7	GND	Ground	22	SR3M	Minus signal of even channel 3 (LVDS)
8	FCLKINM	Minus signal of odd clock channel (LVDS)	23	SR3P	Plus signal of even channel 3 (LVDS)
9	FCLKINP	Plus signal of odd clock channel (LVDS)	24	GND	Ground
10	FR3M	Minus signal of odd channel 3 (LVDS)	25	NC	No Connection.(I2C Serial interface for LCM)
11	FR3P	Plus signal of odd channel 3 (LVDS)	26	NC	No Connection.(I2C Serial interface for LCM)
12	SR0M	Minus signal of even channel 0 (LVDS)	27	NC	Groumd
13	SR0P	Plus signal of even channel 0 (LVDS)	28	VLCD	Power Supply +5.0V
14	GND	Ground	29	VLCD	Power Supply +5.0V
15	SR1M	Minus signal of even channel 1 (LVDS)	30	VLCD	Power Supply +5.0V

Note: 1. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.

- 2. All VLCD (power input) pins should be connected together.
- 3. Input Level of LVDS signal is based on the IEA 664 Standard.
- 4. PWM_OUT signal controls the burst frequency of a inverter.

This signal is synchronized with vertical frequency.

- It's frequency is 3 times of vertical frequency, and it's duty ratio is 50%.
- If you don't use this pin, it is no connection.

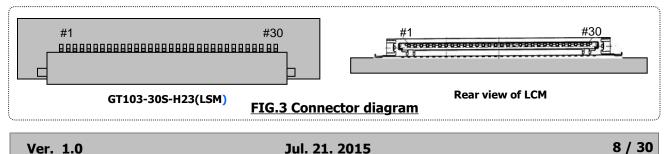




Table 4. REQUIRED SIGNAL ASSIGNMENT FOR Flat Link (TI:SN75LVDS83) Transmitter

Pin #	Pin Name	Require Signal	Pin #	Pin Name	Require Signal
1	Vcc	Power Supply for TTL Input	29	GND	Ground pin for TTL
2	D5	TTL Input (R7)	30	D26	TTL Input (DE)
3	D6	TTL Input (R5)	31	T _X CLKIN	TTL Level clock Input
4	D7	TTL Input (G0)	32	PWR DWN	Power Down Input
5	GND	Ground pin for TTL	33	PLL GND	Ground pin for PLL
6	D8	TTL Input (G1)	34	PLL Vcc	Power Supply for PLL
7	D9	TTL Input (G2)	35	PLL GND	Ground pin for PLL
8	D10	TTL Input (G6)	36	LVDS GND	Ground pin for LVDS
9	Vcc	Power Supply for TTL Input	37	TxOUT3+	Positive LVDS differential data output 3
10	D11	TTL Input (G7)	38	TxOUT3 –	Negative LVDS differential data output 3
11	D12	TTL Input (G3)	39	T _X CLKOUT +	Positive LVDS differential clock output
12	D13	TTL Input (G4)	40	T _X CLKOUT -	Negative LVDS differential clock output
13	GND	Ground pin for TTL	41	T _X OUT2+	Positive LVDS differential data output 2
14	D14	TTL Input (G5)	42	T _X OUT2 –	Negative LVDS differential data output 2
15	D15	TTL Input (B0)	43	LVDS GND	Ground pin for LVDS
16	D16	TTL Input (B6)	44	LVDS Vcc	Power Supply for LVDS
17	Vcc	Power Supply for TTL Input	45	T _X OUT1+	Positive LVDS differential data output 1
18	D17	TTL Input (B7)	46	T _X OUT1-	Negative LVDS differential data output 1
19	D18	TTL Input (B1)	47	T _X OUT0+	Positive LVDS differential data output 0
20	D19	TTL Input (B2)	48	T _X OUT0-	Negative LVDS differential data output 0
21	GND	Ground pin for TTL Input	49	LVDS GND	Ground pin for LVDS
22	D20	TTL Input (B3)	50	D27	TTL Input (R6)
23	D21	TTL Input (B4)	51	D0	TTL Input (R0)
24	D22	TTL Input (B5)	52	D1	TTL Input (R1)
25	D23	TTL Input (RSVD)	53	GND	Ground pin for TTL
26	Vcc	Power Supply for TTL Input	54	D2	TTL Input (R2)
27	D24	TTL Input (HSYNC)	55	D3	TTL Input (R3)
28	D25	TTL Input (VSYNC)	56	D4	TTL Input (R4)

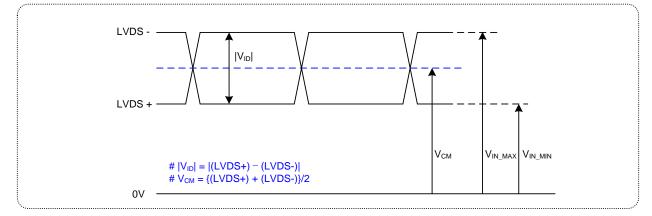
Notes: 1. Refer to LVDS Transmitter Data Sheet for detail descriptions.

2. 7 means MSB and 0 means LSB at R,G,B pixel data



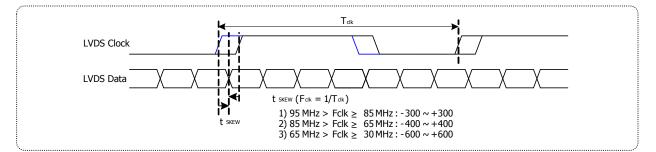
LVDS Input characteristics

1. DC Specification



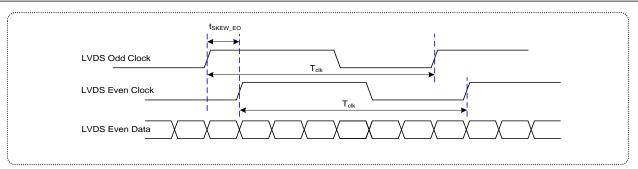
Description	Symbol	Min	Max	Unit	Notes
LVDS Differential Voltage	$ V_{ID} $	200	600	mV	-
LVDS Common mode Voltage	V _{CM}	1.0	1.5	V	-
LVDS Input Voltage Range	V _{IN}	0.7	1.8	V	-
Change in common mode Voltage	ΔVcm	-	250	mV	-

2. AC Specification



Description	Symbol	Min	Max	Unit	Notes
	t _{skew}	- 300	+ 300	ps	95MHz > Fclk ≥ 85MHz
LVDS Clock to Data Skew Margin	t _{skew}	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
	t _{skew}	- 600	+ 600	ps	65MHz > Fclk ≥ 30MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{skew_eo}	- 1/7	+ 1/7	T _{clk}	-





< Clock skew margin between channel >

3. Data Format

1) LVDS 2 Port

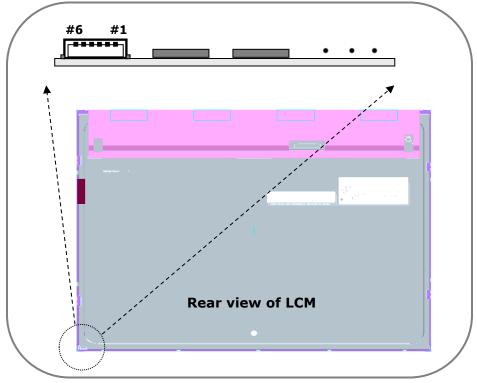
			<			Tclk												
RCLK +			•		<u>(* 4/7</u> Tclk * 1/	7	↓ 1	[clk * 3/]	∕→						Г	MSB	R7]
RXinO0 +/-	OR3	OR2	OR1	OR0	OG0	OR5	OR4	OR3	OR2	OR1	OR0	OG0	OR5	OR4	L		R6 R5	
RXinO1 +/-	OG4	OG3	OG2	OG1	OB1	ОВО		OG4	OG3	OG2	OG1	OB1	ОВО	OG5			R5 R4	
RXinO2 +/-	OB5	OB4	ОВЗ	OB2	DE	VSYNC	HSYNC	OB5	OB4	ОВЗ	OB2	DE	VSYNC	HSYNC			R3 R2	
RXinO3 +/-	OG7	OG6	OR7	OR6	x	ОВ7	OB6	OG7	OG6	OR7	OR6	×	ОВ7	OB6	-		R1	
RXinE0 +/-	ER3	ER2	ER1	ER0	EG0	ER5	ER4	ER3	ER2	ER1	ERO	EG0	ER5	ER4	L	LSB	R0	
RXinE1 +/-	EG4	EG3	EG2	EG1	EB1	EBO	EG5	EG4	EG3	EG2	EG1	EB1	EBO	EG5			D = 1st N = 2nd	
RXinE2 +/-	EB5	EB4	EB3	EB2	DE	VSYNC	HSYNC	EB5	EB4	EB3	EB2	DE	VSYNC	HSYNC				
RXinE3 +/-	EG7	EG6	ER7	ER6	×	EB7	EB6	EG7	EG6	ER7	ER6	×	EB7	EB6				
	——Pre	evious(N	l-1)th Cy	cle	\leftarrow		—Curre	ent(Nth)	Cycle—		\longrightarrow	←Next(N+1)th	Cycle—				

< LVDS Data Format >

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN2)

The LED interface connector is a model 05010HR-06C(G) manufactured by YEONHO. The pin configuration for the connector is shown in the table below.

Pin	Symbol	Description	Notes
1	FB1	Channel1 Current Feedback	
2	FB2	Channel2 Current Feedback	
3	VLED	LED Power Supply	
4	VLED	LED Power Supply	
5	FB3	Channel3 Current Feedback	
6	FB4	Channel4 Current Feedback	



[Figure 5] Backlight connector View

3-3. Signal Timing Specifications

This is signal timing required at the input of the TMDS transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Period	tclk	14.7	18.5	23.6	ns	
DCLK	Frequency	-	42.3	54.0	68.4	MHz	
	total	thp	688	844	940	tclk	
	Frequency	fн	49.4	64.0	81.3	KHz	
Horizontal	Blanking		48	204	300	tclk	
	valid	twн	640	640	640	tсlк	
	total	tvp	1040	1066	1320	thp	
Vertical	Frequency	fv	47	60	76	Hz	
Vertical	Blanking		16	42	296	tHP	
	valid	twv	1024	1024	1024	tHP	

Table 6. TIMING TABLE

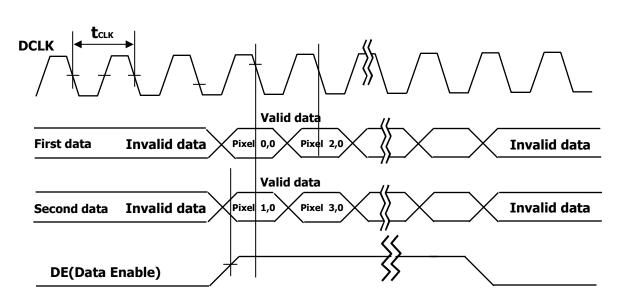
Note:

- 1. DE Only mode operation. The input of Hsync & Vsync signal does not have an effect on LCD normal operation.
- 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
- 3. Horizontal period should be even.

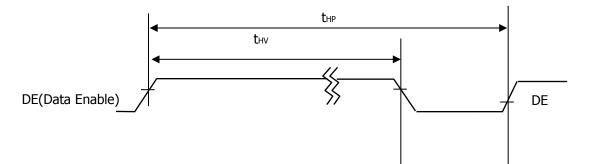


3-4. Signal Timing Waveforms

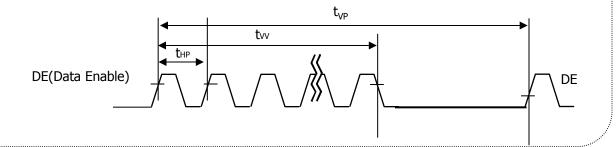
1. DCLK, DE, DATA waveforms



2. Horizontal waveform



3. Vertical waveform



3-5. Color Input Data Reference

The Brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

											-	I	npu	t Co	olor	Dat	ta									
	Color					RE	D							GRI	EEN							BL	UE			
			MS								MS							SB								.SB
	I			_			-		R1			_	-	_	_	G2	_	_		B6		_	_	_	_	
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)		1	1	1	1	1	1	1	1		0	0	0	0	0	0	0		0	0	0	0	0	0	0
	Green (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED							•																			
	RED (254)		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																										
	GREEN (254)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																										
	BLUE (254)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



3-6. Power Sequence

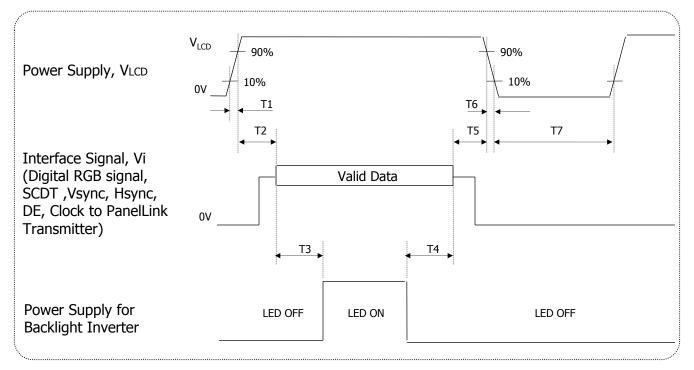


FIG.5 Power sequence

Table 8. POWER SEQUENCE

Devenueter		Values		Units
Parameter	Min	Тур	Мах	Units
T1	0.5	-	10	ms
T2	0.01	-	50	ms
ТЗ	500	-	-	ms
T4	200	-	-	ms
Т5	0.01	-	50	ms
Τ7	1000		-	ms

Notes : 1. Please avoid floating state of interface signal at invalid period.

2. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{LCD} to 0V.

3. LED power must be turn on after power supply for LCD and interface signal are valid.



3-7. V_{LCD} Power Dip Condition

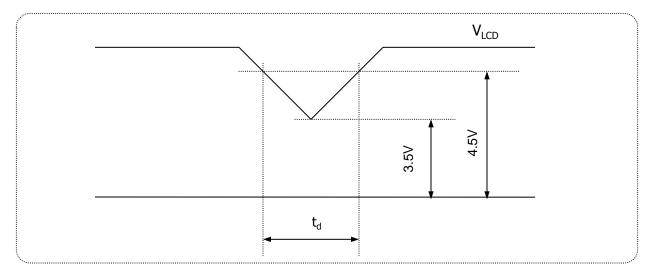


FIG.6 Power dip condition

1) Dip condition

 $3.5V \leq V_{LCD} <$ 4.5V , $t_d {\leq} 20ms$

2) V_{LCD}< 3.5V

 V_{LCD} -dip conditions should also follow the Power On/Off conditions for supply voltage.



4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' for approximately 15 minutes in a dark environment at $25\pm2^{\circ}$ C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 ° and aperture 1 degree.

FIG. 1 presents additional information concerning the measurement equipment and method.

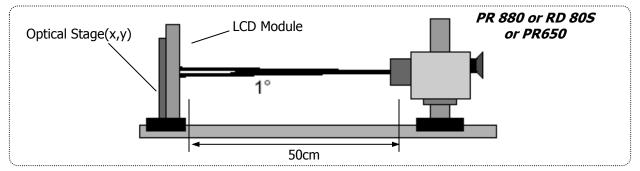


FIG.7 Optical Characteristic Measurement Equipment and Method

(Ta=25 °C, V_{LCD}=5V, f_v=60Hz, Dclk=54MHz, Is=130mA)

Damana		Comula al		Values		llu:ha	Natas
Parame	eter	Symbol	Min	Тур	Max	Units	Notes
Contrast Ratio		CR	600	900	-		1
Surface Luminance, (white	L _{WH}	600	700	-	cd/m ²	2
Luminance Variation (By PR880)		δ_{WHITE}	-	10	20	%	3
Response Time	Gray to Gray	T _{GTG_AVR}	-	14	28	ms	4
Color Coordinates [CIE1931]	WHITE	Wx	Тур	0.280	Тур		-
(By PR650)		Wy	-0.03	0.310	+0.03		-
Color Uniformity (By PR650)	WHITE	Δu′v′	-	-	0.012		5
Viewing Angle (CR>:	10)						
Canada	Horizontal	θ_{H}	170	178	-	Dura	ć
General	Vertical	θ_V	170	178	-	Degree	6
Gray Scale	-	-		2.2			7



Notes 1. Contrast Ratio(CR) is defined mathematically as : (By PR880)

 $Contrast Ratio = \frac{Surface Luminance with all white pixels}{Surface Luminance with all black pixels}$

It is measured at center point(Location P1)

- 2. Surface luminance(LWH) is luminance value at Center 1 point(P1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG.8 (By PR880)
- 3. The variation in surface luminance , δ WHITE is defined as : **(By PR880)**

$$\delta_{WHITE} = Max \left| 1 - \frac{E1;E2;E3;E4}{M1} \right| \times 100 (\%)$$

Where E1 to E4 and M1 are the luminance with all pixels displaying white at 5 locations. For more information see FIG.8

- 4. Gray to gray response time is the time required for the display to transition from gray to gray. For additional information see Table 10. (By RD80S)
- 5. Color uniformity is defined as below Color difference ($\Delta u'v'$). Color difference must be lower than 0.012(TCO Specification). (Measurement points are 5points for FIG.8 Measure Point for Luminance.)

$$u' = \frac{4x}{-2x + 12y + 3} \qquad v' = \frac{9y}{-2x + 12y + 3}$$

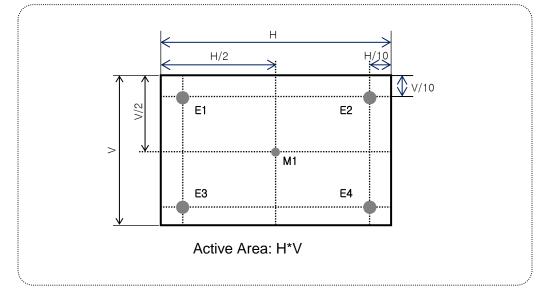
$$\Delta u'v' = \sqrt{(u'_1 - u'_2)^2 + (v'_1 - v'_2)^2}$$

$$u'_1, v'_1 : \text{max. value at front } (\theta=0)$$

$$u'_2, v'_2 : \text{min. value at front } (\theta=0)$$

- 6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.10 (By PR880)
- 7. Gamma Value is approximately 2.2. For more information see Table 11.





Measuring point for surface luminance & measuring point for luminance variation.

FIG.8 Measure Point for Luminance

The Gray to Gray response time is defined as the following figure and shall be measured by switching the input signal for "Gray To Gray ".

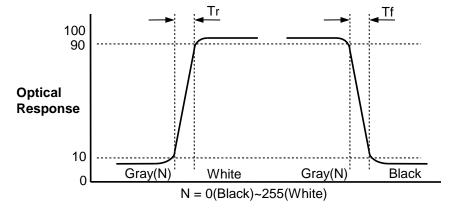
- Gray step : 5 Step
- TGTG_AVR is the total average time at rising time and falling time for "Gray To Gray".
- if system use ODC (Over Driving Circuit) function, Gray to Gary response time may be 5ms~8ms GtG * it depends on Overshoot rate.

Crow to C			R	ising Tim	е	
Gray to G	гау	G255	G191	G127	G63	G0
Falling Time	G255					
	G191		\backslash			
	G127			/		
	G63				/	
	G0					\backslash

Table. 10 GTG Gray Tabl



G to G(BW) Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Black or White".



Dimension of viewing angle range.

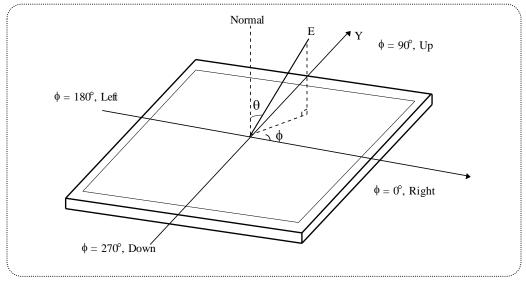


FIG.10 Viewing angle



Table 11. Gray Scale Specification

Gray Level	Relative Luminance [%] (Typ.)
0	0.11
31	1.08
63	4.72
95	11.49
127	21.66
159	35.45
191	53.00
223	74.48
255	100



5. Mechanical Characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

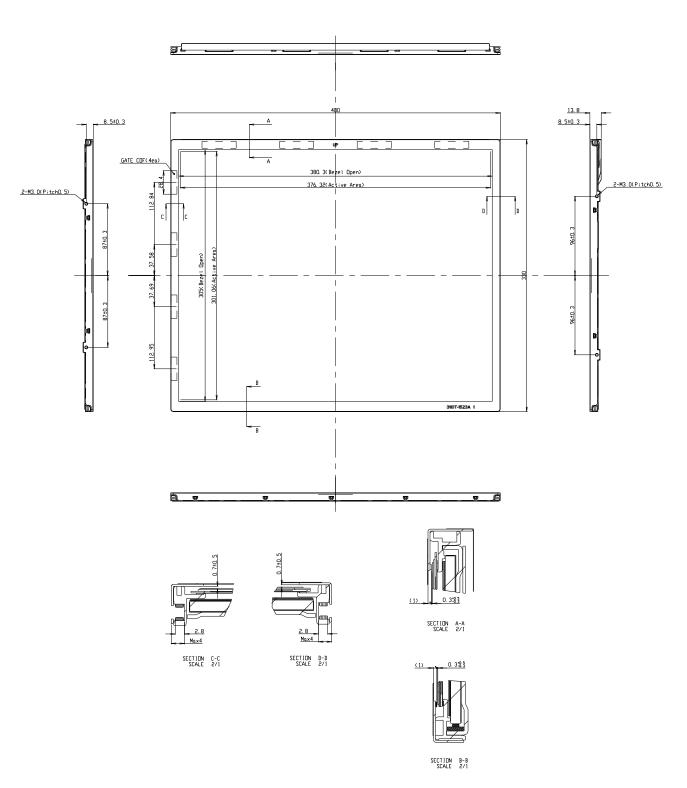
Outline Dimension	Horizontal	400.0mm				
	Vertical	330.0mm				
	Depth	13.8mm				
Bezel Area	Horizontal	380.3mm				
	Vertical	305.0mm				
Active Display Area	Horizontal	376.32mm				
	Vertical	301.06mm				
Weight	Typ. 1550g / Max. 1600g					
Surface Treatment	Hard coating(3H) Advanced Anti-Glare treatment of the front polarizer					

Notes : Please refer to a mechanic drawing in terms of tolerance at the next page.



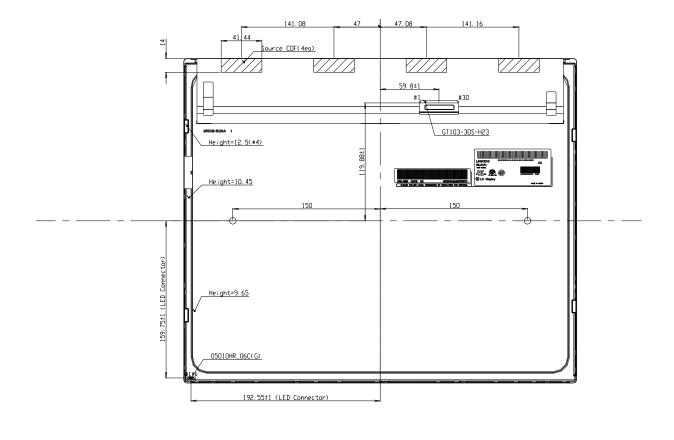
<FRONT VIEW>

Note) Unit:[mm], General tolerance: ± 0.5mm





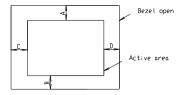
<REAR VIEW>



NOTES

- 1. Backlight has 1 LED Array Ass'y 2. I/F Connector Specification : GT103-30S-H23

- Try connector Specification : C1103-305-H23
 LED Connector Specification : YEONHO 05010 HR-06C(G)
 Torque of user hole : 3.0~4.0kgf-cm
 Tilt and partial disposition tolerance of display area as following (1) Y-Direction : IA-BI < 1.0
 X-Direction : IC-DI < 1.0



6. Unspecified tolerances to be ±0.5mm
7. The COF area is weak & sensitive, So, please don't press the COF area.
8. Outline dimension does not include tape and cover shield thickness.



6. Reliability

Environment test condition

No	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.00G RMS Bandwidth : 10-300Hz Duration : X, Y, Z, 10 min One time each direction
6	Shock test (non-operating)	Shock level : 100G Waveform : half sine wave, 2ms Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction
7	Humidity condition Operation	Ta= 40 °C ,90%RH 240h
8	Altitude operating storage / shipment	0 - 16,000 feet(4,876m) 0 - 40,000 feet(12,192m)

{ Result Evaluation Criteria }

1. Evaluation should be tested after storage at room temperature for 24 hours.

2. There should be no change which might affect the practical display function when the display test quality test is conducted under normal operating condition.

7. International Standards

7-1. Safety

a) UL 60950-1, Underwriters Laboratories Inc.

Information Technology Equipment - Safety - Part 1 : General Requirements. b) CAN/CSA-C22.2 No. 60950-1-07, Canadian Standards Association.

- Information Technology Equipment Safety Part 1 : General Requirements.
- c) EN 60950-1, European Committee for Electrotechnical Standardization (CENELEC). Information Technology Equipment - Safety - Part 1 : General Requirements.

7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

7-3. Environment

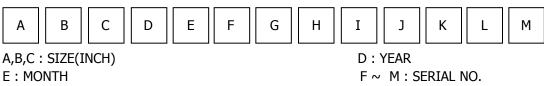
a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark



Note

1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	Н	J	К

2. MONTH

Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one box : 12 pcs
- b) Box Size : 408 X 355 X 474mm



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.
- (10) As The IPS panel is sensitive & slim, please recommend the metal frame of the system supports the panel by the double side-mount.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}(\text{Over and under shoot voltage})$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) When LCMs are used for public display defects such as Yogure, image sticking can not be guarantee.

9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.